COMP 201



UNIVERSITY EXAMINATIONS <u>SECOND YEAR EXAMINATION FOR THE AWARD OF THE DEGREE OF</u> <u>BACHELOR OF SCIENCE IN INFORMATION TECHNOLOGY</u> <u>SECOND SEMESTER 2023/2024</u> [JAN – APRIL, 2024]

COMP 201: BASIC CIRCUIT SYSTEMS

STREAM: Y2 S2

TIME: 2 HOURS

(4 marks)

DAY: FRIDAY, 12:00 - 2:00 P.M.

DATE: 19/04/2024

INSTRUCTIONS

1. Do not write anything on this question paper.

f. Draw a truth table for F1 where F1= x + y'z

2. Answer question ONE (Compulsory) and any other TWO questions.

QUESTION ONE

a. Briefly explain the following terms as used in basic circuit design

Digital system	(2 marks)
Basic latch	(2 marks)
Boolean algebra	(2 marks)
cuss the following types of Error – Detecting codes	
Checksums	(3 marks)
Parity	(3 marks)
Block parity	(3 marks)
i) Explain five characteristics of digital system	(5 marks)
ii) Briefly explain four advantages of digital system over Analog sy	ystem
	(4 marks)
plify $F = x'yz + x'yz' + xz$	(3 marks)
()	Digital system Basic latch Boolean algebra cuss the following types of Error – Detecting codes . Checksums . Parity . Block parity (i) Explain five characteristics of digital system (ii) Briefly explain four advantages of digital system over Analog sy plify F = x'yz + x'yz' + xz

QUESTION ON TWO

a. Write short notes on the following:

i.	Decimal numbers	(3 marks)
ii.	Binary numbers	(3 marks)
iii.	Octal numbers	(3 marks)
iv.	Hexadecimal numbers	(3 marks)
b. Use two's complement to perform the following arithmetic operation		
c. D	ifferentiate between a full adder and half adder.	(4 marks)

QUESTION ON THREE

- a. By the help of a diagram discuss a combinational circuit and list its design procedure. (8mks)
- b. By the help of graphical symbols explain the following types of gates

i.	AND	(3mks)
ii.	OR	(3mks)
iii.	NOR	(3mks)
iv.	NAND	(3mks)

QUESTION FOUR

a.	Explain	capabilities	of a general	shift register.	(6 marks	3)
a.	Explain	capabilities	of a general	sinit register.	(O mark	- 2

b. Memory structures are crucial in digital design. Discuss the following types of memories used in basic circuit design.

(i) PROM	(2 marks)
(ii) EPROM	(2 marks)
(iii) SRAM	(2 marks)
(iv) DRAM	(2 marks)

c. Briefly explain how communication is achieved between memory and its environment. (6 marks)

QUESTION FIVE

a.	Express the Boolean function $F = A + BC$ as a sum of minterms		
		(4 marks)	
b.	Express the Boolean function $F = xy + x'z$ as a product of maxterms.		
		(4 marks)	
c.	(i) List two limitations of Karnaugh maps	(2 marks)	
	(ii) Reduce the expression f=(A+B)(A+B)(A'+B') using mapping	(5 marks)	
d. Prove that $x'y'z' + x'yz' + xyz' = x'z' + yz'$			